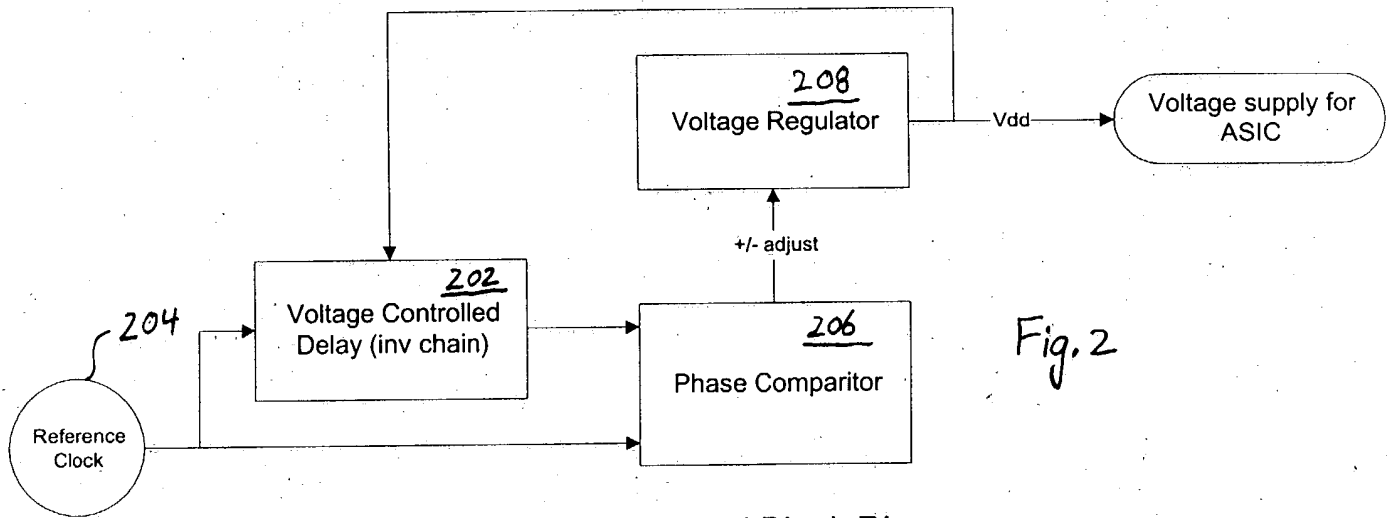


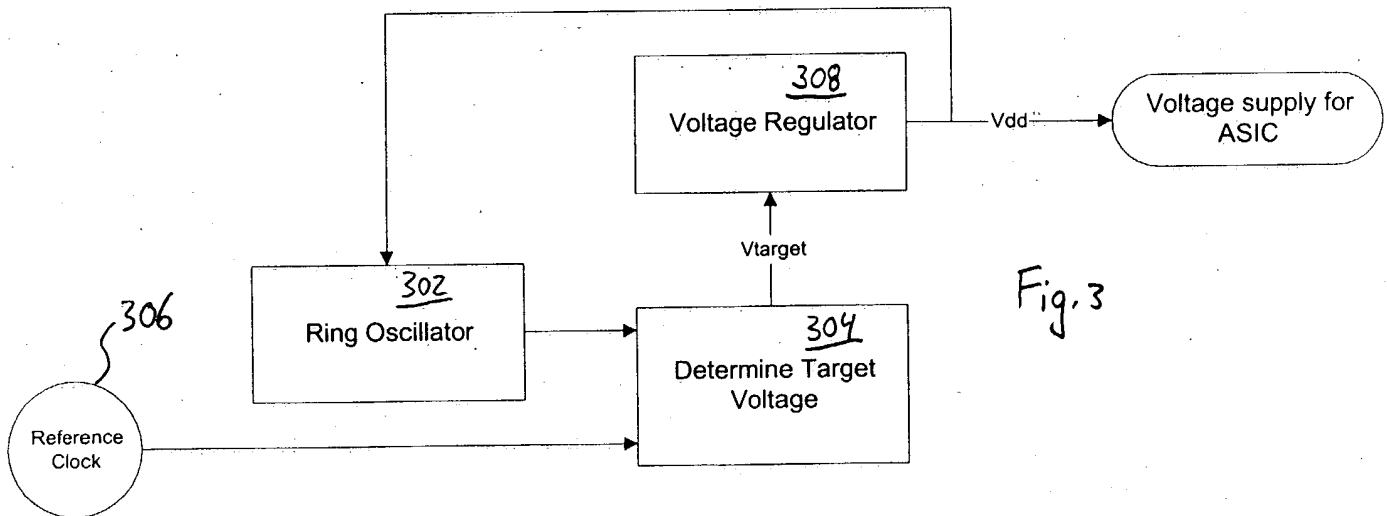
Generic Block Diagram

Fig. 1



Current Digital Block Diagram

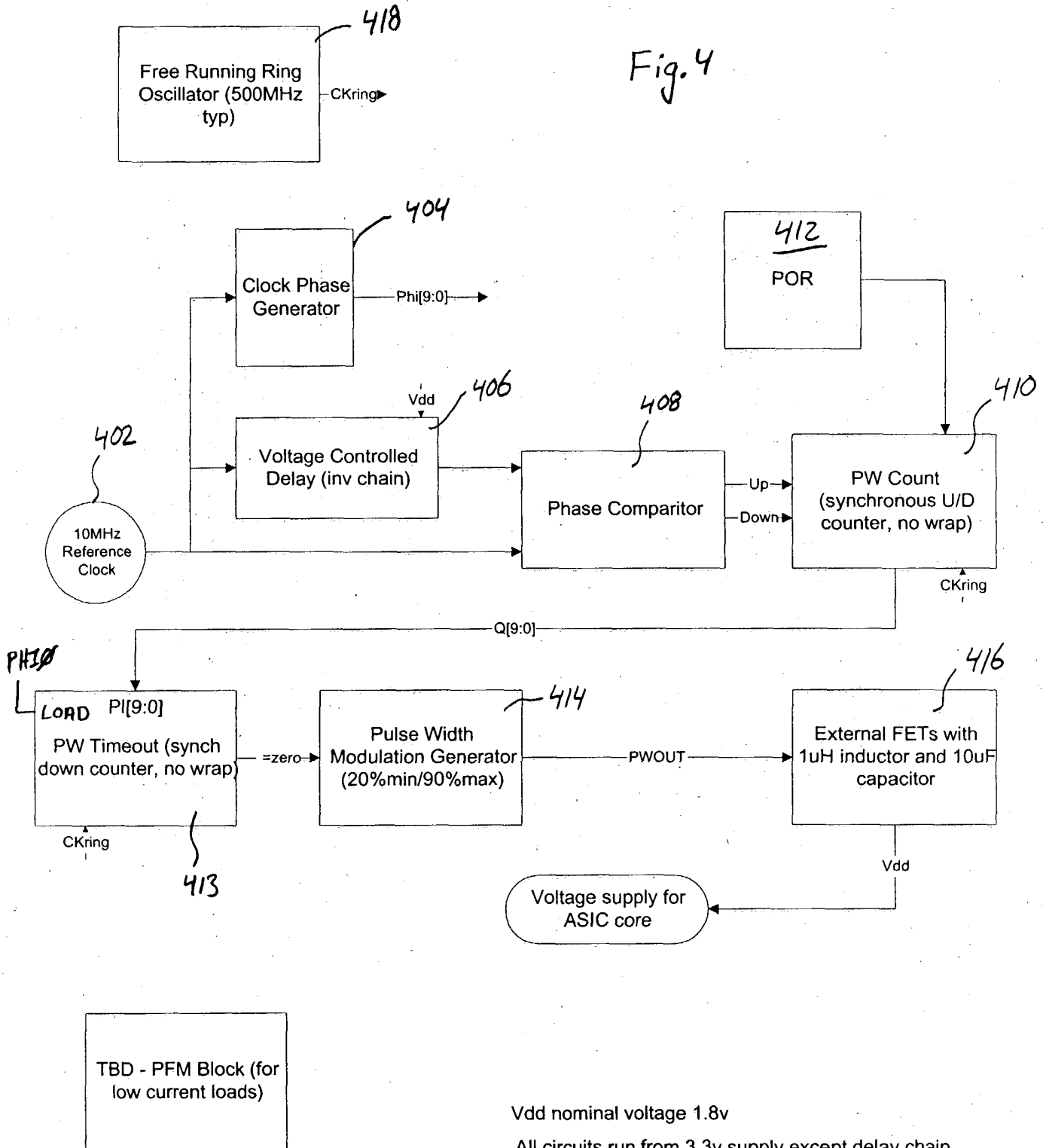
Fig. 2



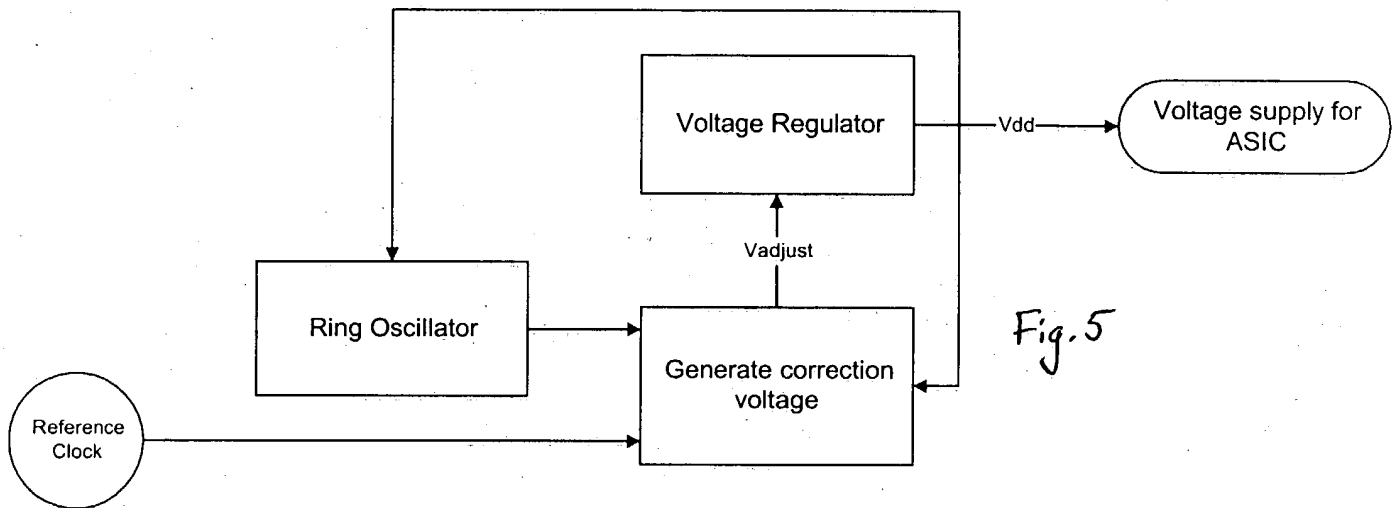
Current Analog Block Diagram

Fig. 3

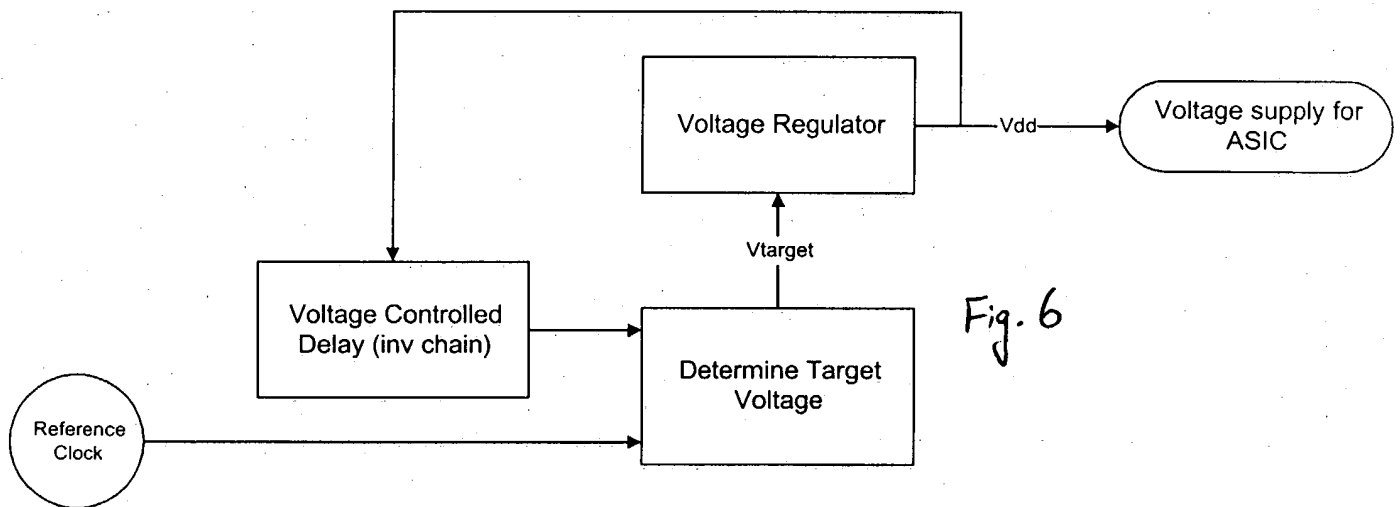
Fig. 4



Current Detailed Digital Block Diagram



Analog Block Diagram - circuit type 1



Analog Block Diagram - circuit type 2

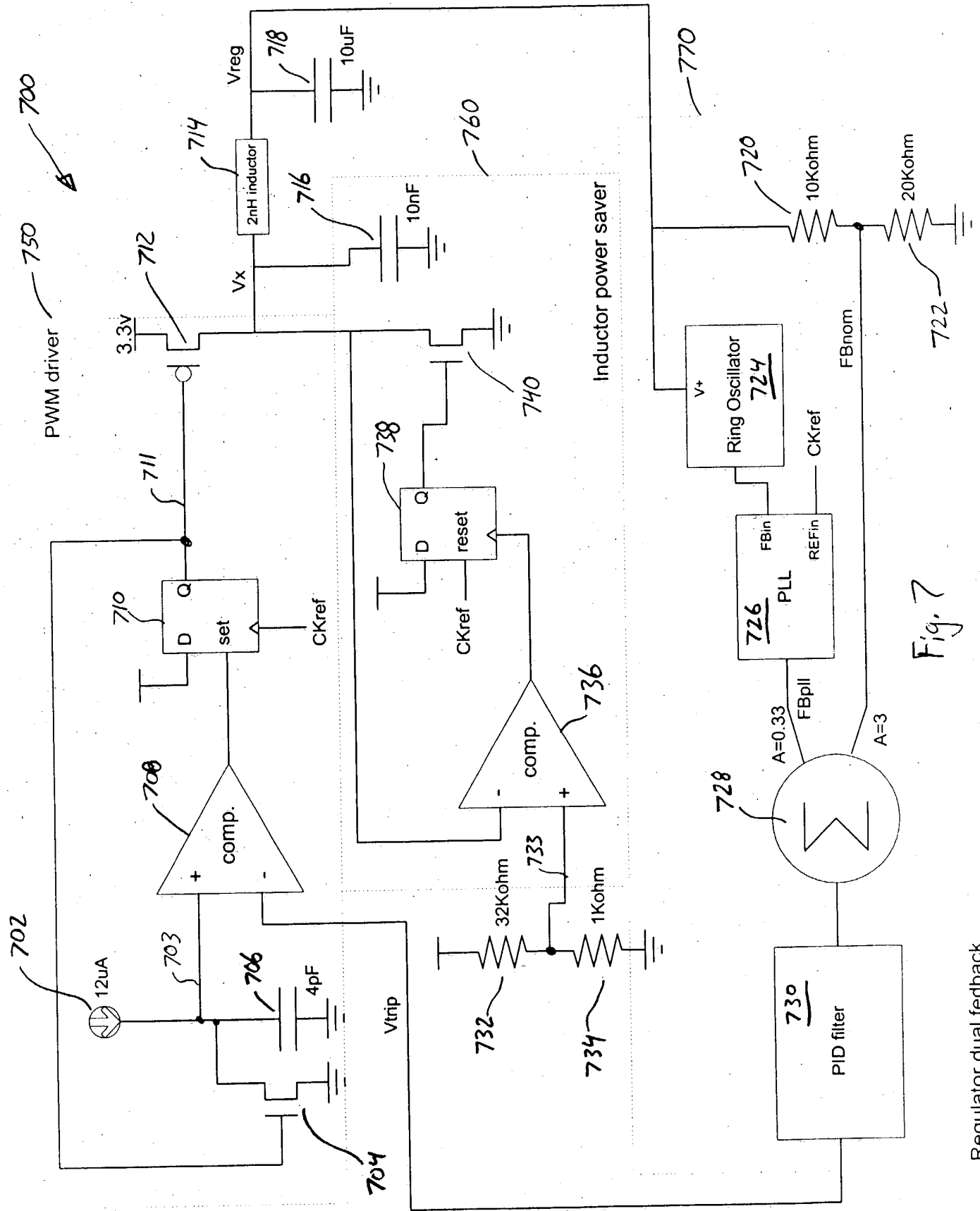


Fig. 7

Regulator dual feedback